

Amendments to the Drawings

The attached replacement sheets of drawings includes changes to Fig. 1A-5H and replaces the original sheets including Figs. 1A-5H.

In Figures 1A-1E, 2A-2D, 4A-4B and 5A-5H, the figure number has been added to each letter.

In Figure 3, the legend, "PRIOR ART" has been added.

Attachments following last page of this Amendment:

Replacement Sheet (5 pages)

Annotated Sheet Showing Change(s) (5 pages)

REMARKS

Claims 1-9, 11-30, 32-36 and 44-46 are pending in the application, with claims 1-4, 12 and 13 being independent. Claims 1, 4 and 12 have been amended and the specification has been amended to move the brief description of the drawings to its proper location. No new matter has been introduced.

Initially, applicant acknowledges with appreciation the allowance of claims 2, 3, 13, 17, 18, 20, 21, 23, 24, 26, 27, 30, 32, 33, 36 and 44-46, and the Examiner's indication that claims 4, 19, 22, 25, 28 and 34 are directed to allowable subject matter.

Applicant also notes that You (U.S. Patent No. 6,777,913), which is used as the basis of a rejection, was not listed on the Notice of References Cited (PTO-892) and has not been previously submitted in an information disclosure statement. Accordingly, applicant respectfully requests that a Form PTO-892 be issued to include this reference.

The Examiner has objected to the disclosure as failing to include a description of the drawings in the proper location. In response to this objection, applicant has moved the "Brief Description of the Drawings" from page 23 to page 6.

The Examiner states that Fig. 3 should be designated as "PRIOR ART." In reply to this objection, Fig. 3 has been amended to add the legend "PRIOR ART." In addition, Figs. 1, 2, 4 and 5 have been amended to properly label them as Figs. 1A-1E, 2A-2D, 4A and 4B, and 5A-5H.

Claims 4, 19, 22, 28 and 34 have been rejected under 35 U.S.C. §112, second paragraph, because "n" is not defined in claim 4 and because the " k^{th} transistor" is allegedly unclear since, if k equals 2, then the second transistor should be " k^{nd} " and should not be " k^{th} ". Applicant has amended claim 4 to recite that n is a natural number. As to the objection to the use of " k^{th} ", applicant submits that the notation is entirely clear and that no reasonable alternative is available. Moreover, contrary to the assertion in the rejection, if k equals 2, then the " k^{th} " transistor would be the "second" transistor rather than the " k^{nd} " transistor, as would be readily understood by one of ordinary skill in the art. Accordingly, for at least these reasons, applicant requests that the rejection be withdrawn.

Claims 1 and 8 have been rejected as being anticipated by Engel (U.S. Patent No. 5,734,237). Applicant requests reconsideration and withdrawal of this rejection because Engel does not describe or suggest an arrangement in which the “the electronic circuit amplifies an inputted current for output,” as recited in amended claim 1. Instead, Engel describes an integrated charger/controller that operates in a charge mode in which a battery is charged and a drive mode in which the battery drives a motor. Accordingly, the rejection should be withdrawn.

Claims 11, 12, 14-16, 19 and 35 have been rejected as being unpatentable over You (U.S. Patent No. 6,777,913) in view of Engel. Like claim 1, independent claim 12 has been amended to recite that the electronic circuit amplifies an inputted current for output. Accordingly, applicant requests reconsideration and withdrawal of the rejection of claim 12 and its dependent claims (and claim 11, which depends from claim 1) because Engel, as discussed above, does not describe or suggest this feature and because You, which is cited as showing a personal computer having a charger, does not remedy this failure of Engel.

Claims 5-7 and 9, which depend from claim 1, have been rejected as being unpatentable over Engel in view of Dawson (WO 98/48403). Applicant requests reconsideration and withdrawal of this rejection because Dawson, which is cited as showing p-channel and n-channel transistors, does not remedy the failure of Engel to describe or suggest the subject matter of claim 1.

Applicant submits that all claims are in condition for allowance.

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The fee in the amount of \$120 in payment of the one-month extension fee is being paid concurrently herewith on the Electronic Filing System (EFS) by way of Deposit Account authorization. Please apply any other charges or credits to Deposit Account No. 06-1050.

Respectfully submitted,

Date: 10/29/07



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~~FIG. 1~~

FIG. 1A

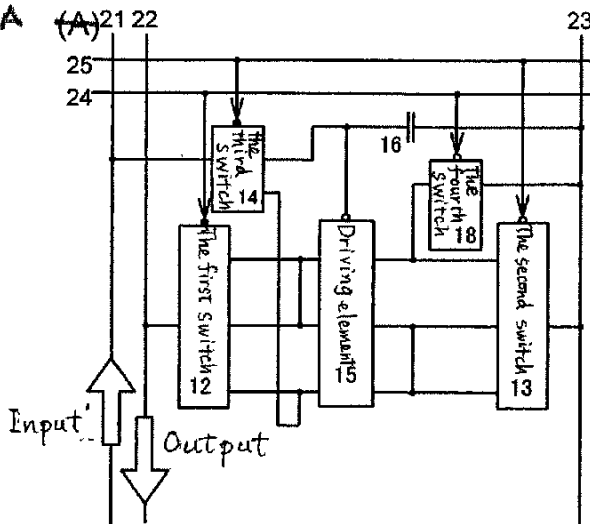


FIG. 1B

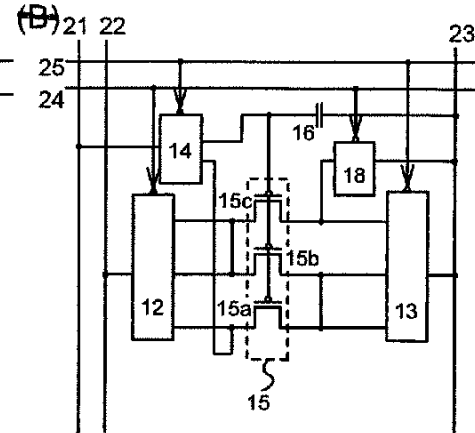


FIG. 1C

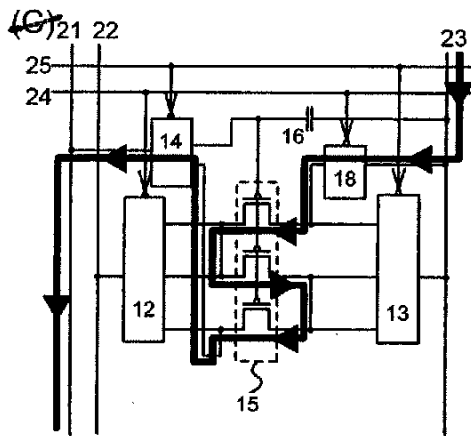


FIG. 1D

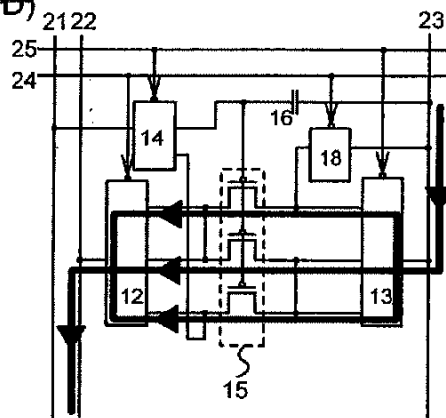
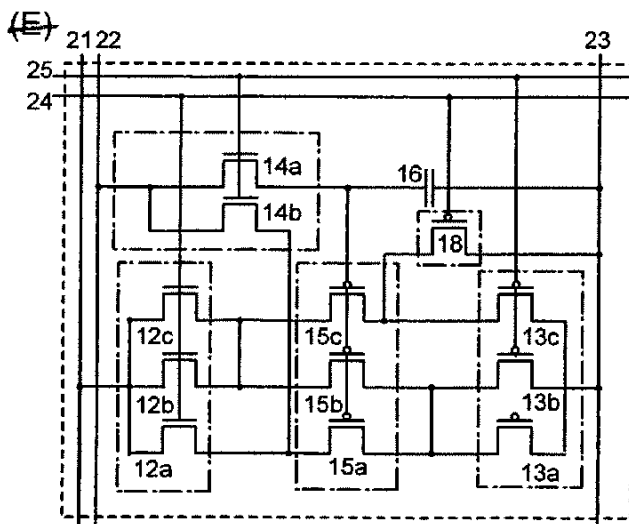
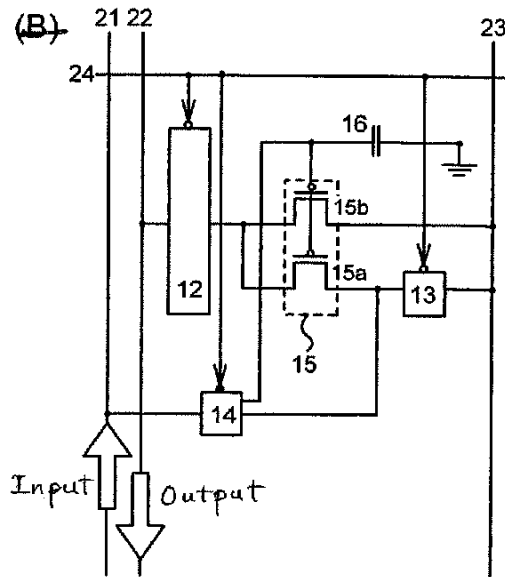
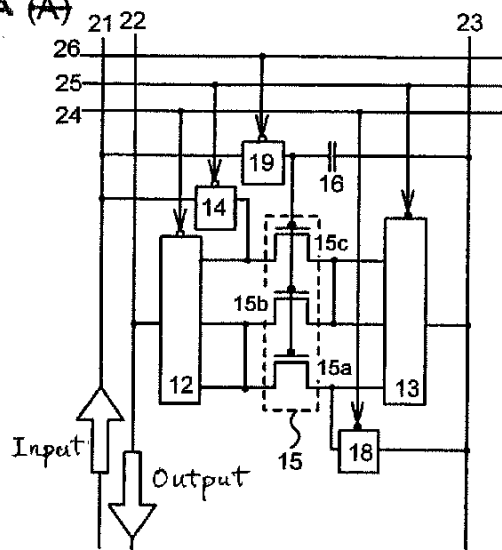


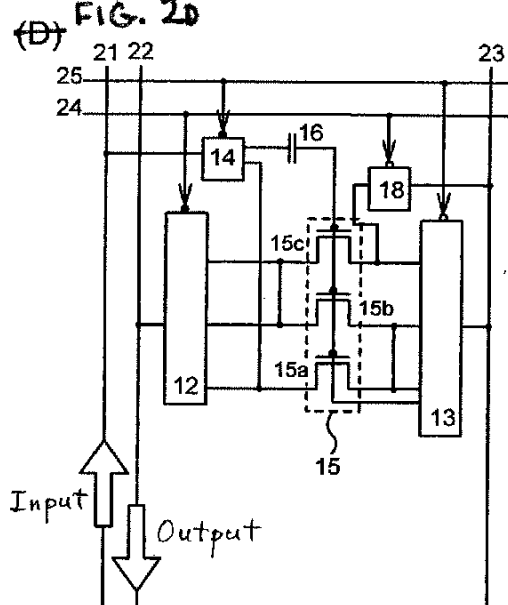
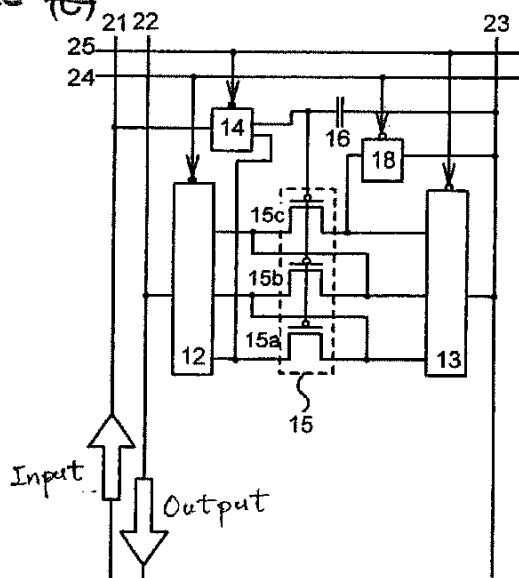
FIG. 1E



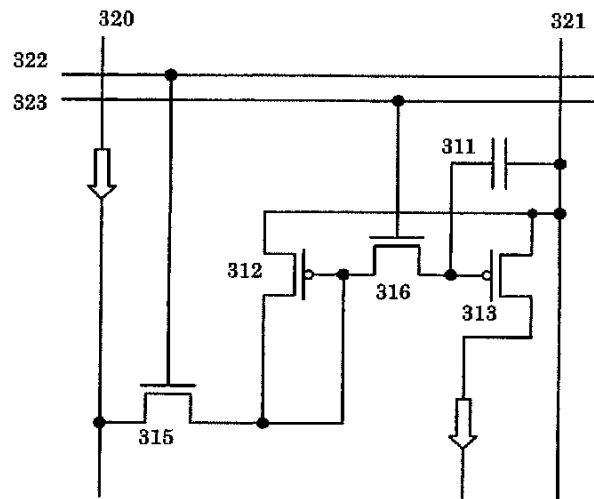
~~(B)~~



(D) FIG. 2d



[FIG. 3]
PRIOR ART



~~FIG. 4~~
FIG. 4A
(A)

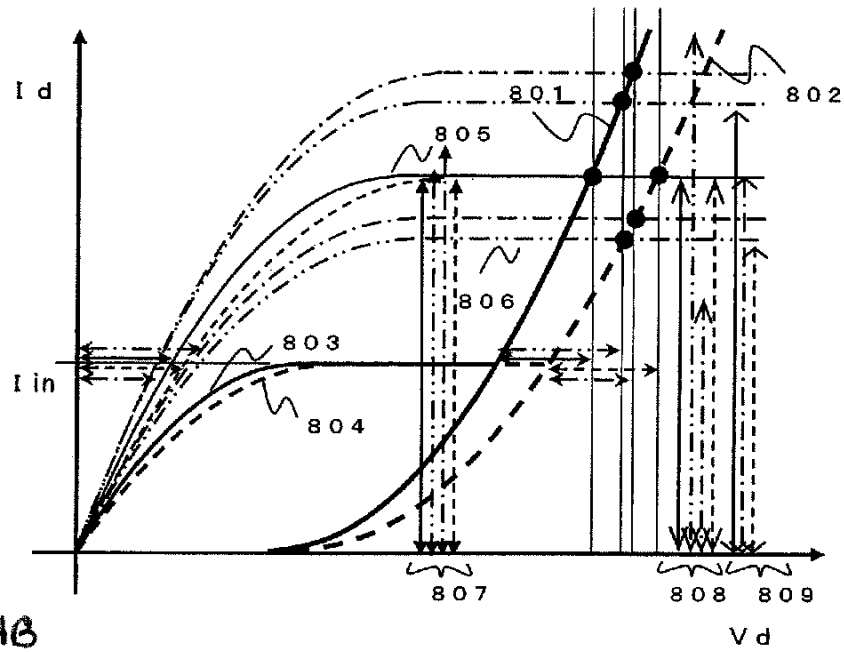
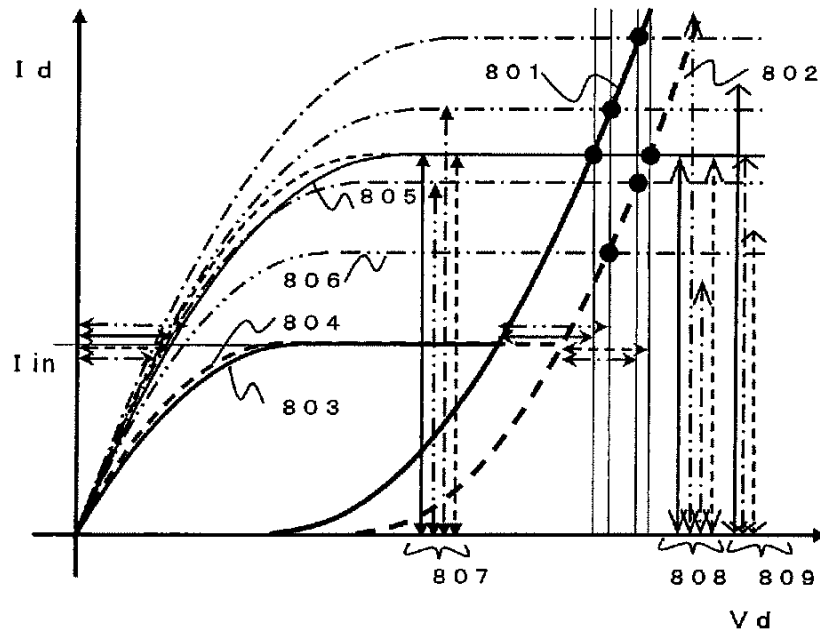


FIG. 4B
(B)



~~FIG. 5~~

FIG. 5A

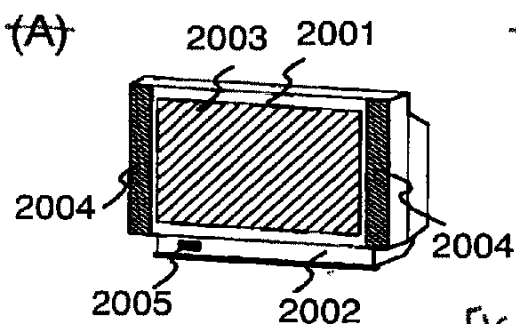


FIG. 5B

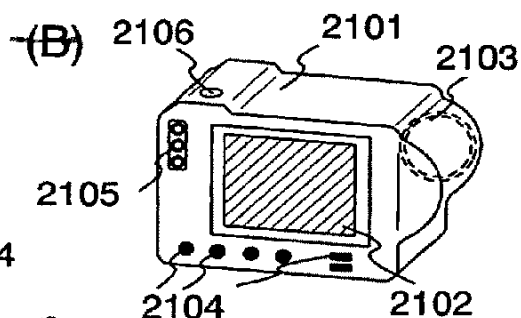


FIG. 5C

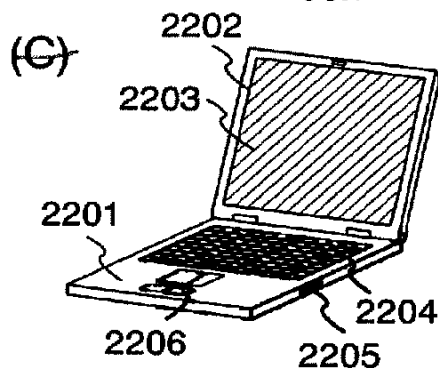


FIG. 5D

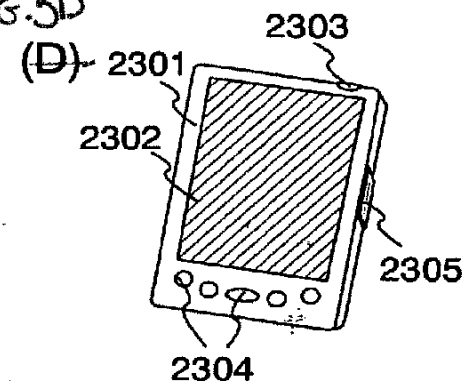


FIG. 5E

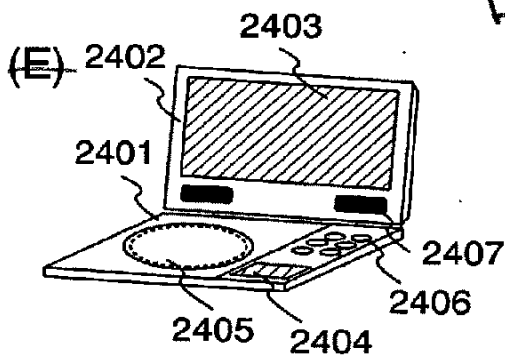


FIG. 5F

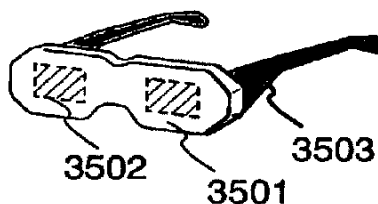


FIG. 5G

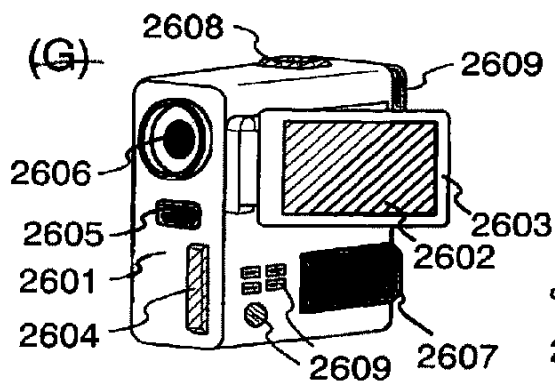


FIG. 5H

